

PATENT APPLICATION DATA SHEET

(Under 37 C.F.R. § 1.76.)

Application Information

Application Type::	Regular
Subject Matter::	Utility
Suggested Classification::	Unknown
Suggested Group Art Unit::	Unknown
CD-ROM or CD-R?::	No
Title::	System for Measuring Signal Path Resistance For An Integrated Circuit Tester Interconnect Structure
Attorney Docket Number::	P103D1-US
Request For Early Publication?::	No
Request For Non-Publication?::	No
Suggested Drawing Figure::	6
Total Drawing Sheets::	5
Small Entity?::	No
Petition Included?::	No
Secrecy Order In Parent Appl.?::	No

Inventor Information

Inventor Authority Type::	Inventor
Primary Citizenship Country::	US
Status::	Full Capacity
Given Name::	John
Middle Name::	M.
Family Name::	Long
Name Suffix::	
City of Residence::	San Jose
State or Prov. of Residence::	CA
Country of Residence::	US

Street:: 16211 Highland Drive
City:: San Jose
State or Province:: CA
Country:: US
Postal or Zip Code:: 95127

Correspondence Information

Correspondence Customer Number: 000027520

Representative Information

Representative Customer Number:: 000027520

Domestic Priority Information

Application::	Continuity Type::	Parent	Parent Filing
<i>This Application</i>	<i>Is a Divisional of</i>	Application::	Date::
		09/548,886	4/13/2000

Foreign Priority Information

Country:: Application Number:: Filing Date::

Assignee Information

Assignee Name:: FormFactor, Inc.